

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	327	(adjust\$3 with voltage) same (test\$3 with memory)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/26 16:12
S2	19	S1 and ((pullup or pull-up) with transistor\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/29 15:37
S3	10	365/201,200.ccls. and test\$3 and ((pullup or pull-up) with transistor\$1) and SRAM and ((modif\$4 or adjust\$3 or variable\$1) with bias)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/29 15:41
S6	53	365/201,200,230.06,226,190, 203-205,194,154,156.ccls. and (weak with test) and SRAM and transistor\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/29 15:44
S7	5	365/201,200,230.06,226,190, 203-205,194,154,156.ccls. and (weak with test) and SRAM and (transistor\$1 with (fuse\$1 or antifuse\$1))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/29 15:45
S8	51827	SRAM and memory	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/26 16:18
S9	15872	S8 and test\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/29 15:45
S10	25	S9 and ((weak adj write adj test adj mode) or WWTM)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/29 15:46

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S11	325	S9 and bias near2 generat\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/29 15:47
S12	232	S11 and driver	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/29 15:47
S13	232	S12 and voltage	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/29 15:47
S14	226	S13 and transistor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/29 15:48
S15	113	S14 and (pullup or pull-up)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/29 15:48
S16	98	S15 and (pulldown or pull-down)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/29 15:49
S17	69	S16 and metal\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/29 15:49
S18	3	S9 and (metal-program\$4 or (metal adj program\$4))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/29 15:50

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S19	27	test\$3 and (metal-program\$4 or (metal adj program\$4))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/29 15:51
S20	16	S19 and memory	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/29 15:50
S21	9	S20 and (bias\$3 or voltage\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/29 15:51
S22	4	(bias adj2 generator) and (metal-program\$4 or (metal adj program\$4))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/29 15:53
S23	4966	circuit\$3 same (internal adj voltage)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/29 16:04
S24	34	S1 and S23	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/29 15:54
S25	49	circuit\$3 same (internal adj voltage) same ((pullup or pull-up) with transistor\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/29 16:05
S26	16	circuit\$3 same (internal adj voltage) same ((pullup or pull-up) with transistor\$1) same ((pulldown or pull-down) with transistor\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/29 16:05

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S27	2151	SRAM and ((bias or voltage) with generator)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/26 16:37
S28	18	S27 and adjust\$3 near5 output near5 program\$6	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/26 16:26
S29	42	S27 and adjust\$3 with output with program\$6	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/26 16:26
S30	2297	SRAM and ((bias or voltage or potential) with generator)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/26 16:28
S31	28	S30 and adjust\$3 with output with (program\$4 or divider)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/26 16:34
S32	86	S30 and adjust\$3 same output same (program\$4 or divider)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/26 16:34
S33	5	SRAM and ((bias or voltage) with generator) with adjust\$3 with output\$3 with (program\$4 or divider or resistor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/26 16:42
S34	50	SRAM and ((bias or voltage) with generator) and (adjust\$3 with output\$3 with (program\$4 or divider or resistor))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/26 16:42

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S35	2	"6,025737".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/07 17:49
S36	0	2004/0119112	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/07 17:50
S37	2	"20040119112"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/07 17:55
S38	2	"6,178113".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/07 17:55